

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of: **Koji TAKAHASHI**

Serial Number: **Not Yet Assigned**

Filed: **August 28, 2003**

Customer No.: **23850**

For: **METHOD OF MANUFACTURING A MEMORY INTEGRATED CIRCUIT  
DEVICE**

**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

August 28, 2003

Sir:

In compliance with 37 CFR 1.56, Applicant calls to the attention of the Patent and Trademark Office the reference listed on the attached PTO-1449.

A copy of the reference is enclosed herewith.

In the event there are any fees due in connection with the filing of this paper, please charge Deposit Account No. 01-2340.

Respectfully submitted,  
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Enclosures: PTO-1449; Reference (1)

<b>INFORMATION DISCLOSURE STATEMENT PTO-1449</b>	Atty. Docket No. 031038	Serial No. New Appln.
	Applicant(s): Koji TAKAHASHI	
	Filing Date: August 28, 2003	Group Art Unit:

### U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
_____	AA					
_____	AB					
_____	AC					
_____	AD					

### FOREIGN PATENT DOCUMENTS

Document No.	Date	Country	Translation (Yes or No)		
_____	AE	8-186183	07/16/96	Japan	Abstract.
_____	AF				
_____	AG				
_____	AH				
_____	AI				

### OTHER DOCUMENTS

_____	AJ			
_____	AK			
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